CLAIMS

1. A regulating system for regulating, with respect to a reference level (Vref), the level of an amplified signal (108), said regulating system comprising :

- attenuation means (201) for generating an attenuated signal (202) from said amplified signal (108) according to a programmable attenuation factor,
- conversion means (203) for converting said attenuated signal (202) in order to generate an output signal (204) intended to be compared with said reference level (Vref).

2. A regulating system as claimed in claim 1, wherein :

- said attenuation means (201) comprise a network of resistances (Rp1, Rs1, Rp2, Rs2, Rp3, Rs3, Rp4, Rs4, Rp5, Rs5) defined by a set of π-structures connected in series, each node (A, B, C, D, E, F) of the π-structures being connected to a switch (SWA, SWB, SWC, SWD, SWE, SWF) intended to be activated for defining said programmable attenuation factor,
- said conversion means (203) comprise processing means (401, 402) for generating said output signal (204) with a level proportional to the square of the effective value of said attenuated signal (202).

3. A regulating system as claimed in claim 2, wherein the switches (SWA, SWB, SWC, SWD, SWE, SWF) are intended to be activated by a command word (SA, SB, SC, SD, SE, SF) delivered by a digital bus (301).

4. A regulating system as claimed in one of claims 1 to 3, comprising a voltage comparator (205) including an adjustable voltage/current converter (403), for generating an output current signal I_{AGC} being proportional to the difference between said output signal (204) and said reference level (Vref).

5. An integrated circuit comprising a regulating system as claimed in one of claims 1 to 4.

6. A tuner comprising a regulating system as claimed in one of claims 1 to 4.